

**INNOPHASE** is a rapidly growing communications semiconductor startup with headquarters located in San Diego, CA. We are developing complete wireless solutions with significantly improved power dissipation/performance tradeoffs as compared to industry competitors. Our innovative technology also dramatically improves wireless product flexibility and ease-of-use for product developers. We are looking for driven candidates to join our fast-paced and motivated team.

In this job you will be responsible for leading a team of engineers to develop test solutions for communications and connectivity SoC products. Your team will develop comprehensive automated test HW and SW solutions that generate accurate, critical performance and functional data for product validation and characterization. Understand and test SoCs with technology, architectures and performance never implemented or achieved.

### Key Responsibilities

- Hands on technical team leadership
- Hiring, resource planning, scheduling, performance management and overall team execution
- Establish SoC verification and characterization test system design, implementation methodologies, strategies and flows
- Contribute to DFT elements of SoC design and definition
- Develop infrastructure, tools and policies for SoC test data reduction and analysis
- Lead team to conduct and deliver relevant data for SoC product verification and characterization
- Work with System, Software, RF and Analog teams to help debug development issues and provide necessary support

### Desirable Skills

- Experience with embedded systems, wireless protocols, power management, signal processing, interfaces (I2C, SPI, SerDes...)...
- Solid board/system level RF, analog, digital and FW/SW development and debug skills
- Experience developing test solutions and testing highly integrated mixed signal SoCs
- Experience developing production ATE solutions for mixed signal SoCs

### Job Requirements

- Experience bringing silicon to commercial mass production
- Strong competency in electrical engineering fundamentals
- Expert in use of common engineering lab test equipment (S/A, VNA, scope, Logic Analyzer...)
- Experience in debugging RF/analog, digital and interface problems at the PCB level
- Strong abilities in test automation and device control FW/SW
- Good understanding of the value and use of configuration management systems like git
- Knowledge of software languages such as C/C++, Perl, Tcl and Python
- Strong knowledge of mixed signal concepts
- Strong communication and presentation skills
- Strong technical leadership skills
- Strong team builder and recruiter
- Ability to distill key requirements from incomplete inputs
- Ability to foresee issues and design in flexibility and workarounds for both known and unknown unknowns
- Team player with strong sense of urgency to complete projects on time and ability to be flexible and creative in this dynamic position

5+ years relevant experience required

MS EE/CS preferred.

[www.InnoPhaseInc.com](http://www.InnoPhaseInc.com)