

INNOPHASE is a rapidly growing communications semiconductor startup with headquarters located in San Diego, CA. It is an exciting time to join Innophase and work with a brilliant team of engineers to design innovative wireless products and solutions for IoT and 5G. If you want to make a big impact with a small team and are looking to expand your skill set, this is the place.

In this job you will be a member of the Development and Product Test Engineering team and a key contributor to design and test of RF hardware circuits and systems from initial concept through production. The RF Hardware Design Engineer must have strong technical skills and knowledge, be passionate about design and innovation, work effectively with limited information, share technical expertise with others and be creative problem solvers and effective communicators.

This is a full-time position based in San Diego. In addition to benefits and perks below, Innophase offers relocation assistance!

Key Responsibilities

- Design ~GHz RFIC test or RF system boards to support wireless communication chipset evaluation using CAD tools for schematic capturing
- Generate BOM and properly select components based on circuit analysis
- Support PCB layout engineer from placement and routing all the way to Gerber file tape out
- Coordinate among internal engineering teams to meet for PCB design requirements on a daily basis
- Perform troubleshooting and support PCB bring up

Desirable Skills

- Good understanding of wireless communication theory, RF transceiver architecture, receiver, transmitter, PA, PLL design and test considerations
- Familiarity with fundamentals of typical RF measurements
- Experience with RF test equipment such as Spectrum analyzer, Network analyser, Vector signal analyzer etc.
- Able to work effectively with incomplete or changing requirements
- Focused, goal driven finisher

Job Requirements

- BSEE/MSEE preferred or equivalent work experience
- 7-10+ years of experience with RF board-level circuit hardware design and test background
- Design and analyze analog and digital RF HW
- Knowledge of PCB schematic capturing and layout tools such as:
 - Schematic Capture - Cadence OrCad/ Concept HDL, Mentor Graphics xDX Designer/ Pads Logic, Altium Designer
 - PCB Layout Design - Mentor Graphics Xpedition, Cadence Allegro/OrCad, Mentor Graphics PADS, Altium Designer

Benefits and Perks

- Competitive salary and stock options
- Learning and development opportunities
- Employer paid health benefits
- Paid vacation/holidays/sick/parental leave
- Flexibility to balance quality work and personal lives
- Annual holiday and summer events
- Free snacks and drinks

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