

**INNOPHASE** is a rapidly growing communications semiconductor startup with headquarters located in San Diego, CA. We are developing complete wireless solutions with significantly improved power dissipation/performance tradeoffs as compared to industry competitors. Our innovative technology also dramatically improves wireless product flexibility and ease-of-use for product developers. We are looking for driven candidates to join our fast-paced and motivated team.

In this job you will be working with a team of engineers to develop novel SoC products for connectivity and communications. You will work across the organization understanding from Product Management and System Engineering the high-level product requirements, with HW/FW Design to contribute to and understand the product implementation and develop and drive a comprehensive verification plan.

### Job Requirements

- 7+ years experience in digital SoC verification required
- MS/PhD EE/CS preferred
- Hands on experience successfully verifying complex SoCs
- Experience simulating mixed signal SoCs with RF/analog, custom RTL and IP digital and FW elements using SystemVerilog & VHDL
- Experience in the development or use of HW emulators for verification
- Ability to understand HW implementations, and FW managed HW techniques
- Ability use FW for product verification (either developing or reusing)
- Experience with embedded systems, wireless protocols, power mgt and signal processing
- Strong in developing Verilog and SystemVerilog test benches and pass/fail criteria
- Experience setting up and exploiting continuous integration techniques
- User of programming languages such as C/C++, Python, and Tcl
- Experience bringing highly integrated mixed signal SoCs to commercial mass production
- Strong communication and presentation skills
- Good skills and interest in recruiting and mentorship

### Key Responsibilities

- Create, manage and drive comprehensive product verification plans
- Evaluate verification coverage, provide gap analyses and mitigation strategies
- Hands on technical leadership
- Contribute to/review SoC specifications and architectures from a verification context
- Work with Systems and Design teams to understand product requirements and implementation (HW/FW)
- Support schedule and resource planning for verification
- Support debug of verification failures

### Desirable Skills

- Knowledge working with low power design, multiple clock domains and other digital SoC intricacies
- Experience with formal verification tools
- Skilled in problem solving and UVM verification
- Knowledge/experience with wireless communications systems (e.g., WiFi, 4G/5G)
- Knowledge of industry standard interfaces
- Able to work effectively with incomplete or changing requirements
- Ability to foresee issues and develop flexibility or workarounds for unknowns

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