

INNOPHASE is a rapidly growing ultralow-power wireless semiconductor startup with headquarters located in San Diego, CA. We are developing complete wireless solutions with significantly differentiated power dissipation/performance tradeoffs. Our innovative technology also dramatically improves wireless product flexibility and ease-of-use for product developers. We are looking for driven candidates to join our fast-paced and motivated team.

RF Systems Engineer: This position entails developing requirements for, analyzing, simulating and validating novel radio systems implemented in silicon SoCs for high performance applications such as LTE and sub-6 GHz 5G cellular base stations. This role is an excellent opportunity for someone that enjoys driving the critical path and making a significant impact in launching products into the market and winning!

Key Responsibilities

- Analyze product system requirements for cellular base station radios and other wireless applications, and distill to radio architecture and performance requirements to the block level
- Develop and exercise system level simulations of radio architectures and implementations at various levels of abstraction
- Work with design and other systems engineers to optimize design and definition through various practical implementation tradeoffs against system operation
- Provide key technical expertise in validating radio implementations in the engineering lab through defining required tests and functions through conducting key systems tests and interpreting results

Desirable Skills

- Deep understanding of cellular air interface and radio requirements and implementations
- Broad experience with analytical tools such as Matlab, Cadence (RTL) simulators, etc.
- Ability to design, drive layout and debug board level radio solutions

MSEE and 10+ years experience desired

Full-time Opportunity

Excellent Benefits

Job Requirements

- Strong fundamentals in communications theory, application and analysis
- Knowledge and experience with modern RF radio architectures and tradeoffs, from superheterodyne to homodyne and RF/IF sampling architectures
- Ability to create system level simulation models from the ground up, including behavioral models of lower level blocks as well as able to construct meaningful overall system simulation environments to drive system design and definition
- Keenly aware and experienced in the power of modern digital processing and firmware advantages and tradeoffs in modern radios
- Good familiarity in analog and RF IC design, especially with modern CMOS technology
- Hands on experience making analog, digital and RF measurements using typical laboratory instruments and techniques
- Analytical and disciplined problem solver
- Team player with a strong sense of urgency to meet product needs on time
- Good verbal and written communication and presentation skills
- Hands on experience making analog, digital and RF measurements using typical laboratory instruments and techniques